

STM32 F4 Timers

TIMER #	TIM1	TIM8	TIM2	TIM5	TIM3	TIM4	TIM9	TIM12	TIM10	TIM11	TIM13	TIM14	TIM6	TIM7
Resolution (bits)	16	16	32	32	16	16	16	16	16	16	16	16	16	16
Prescaler	16	16	16	16	16	16	16	16	16	16	16	16	16	16
Count Up	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Count Down	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X	X	X
Count Up Down	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X	X	X
Input Capture	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
Output Compare	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
PWM	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
One Pulse	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
# IO Channels	4	4	4	4	4	4	2	2	1	1	1	1	X	X
Complementary Outputs	✓	✓	X	X	X	X	X	X	X	X	X	X	X	X
Sync Circuit	✓	✓	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X
Interrupt (Counter overflow)	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
Interrupt (Counter underflow)	✓	✓	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X
Interrupt (Counter Initialization)	✓	✓	✓	✓	✓	✓	X	X	✓	✓	✓	✓	X	X
Interrupt (Trigger)	✓	✓	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X
Interrupt (Input Capture)	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
Interrupt (Output Compare)	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	X	X
Interrupt (Break Input)	✓	✓	X	X	X	X	X	X	X	X	X	X	X	X
Trigger Input	✓	✓	✓	✓	✓	✓	X	X	X	X	X	X	X	X
Driving DAC	X	X	X	X	X	X	X	X	X	X	X	X	✓	✓
DMA trigger (counter overflow)	X	X	X	X	X	X	X	X	X	X	X	X	✓	✓

This table is prepared from the information found in STM32 F4 Reference Manual. (RM0090)
 Some information may be INCORRECT. Use at your own risk!